



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **FUKASAWA, et al.**

Group Art Unit: **2827**

Serial No.: **09/635,124**

Examiner: **GRAYBILL, David E.**

Filed: **August 8, 2000**

P.T.O. Confirmation No.: 4720

For: **METHOD AND MOLD FOR MANUFACTURING SEMICONDUCTOR DEVICE,
SEMICONDUCTOR**

AMENDMENT AFTER FINAL REJECTION

MAILSTOP AF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 3, 2003

Sir:

In response to the Office Action dated **June 3, 2003**, please amend the above-identified application as follows: